



CAD + configuration + circuit + state

Search

[Advanced Scholar Search](#)
[Scholar Preferences](#)
[Scholar Help](#)

Scholar

Results 1 - 10 of about 13,500 for **CAD + configuration + circuit + state**. (0.15 seconds)

[JBits: A Java-based Interface for Reconfigurable Computing](#) - [group of 9 »](#) [All articles](#) [Recent articles](#)

SA Guccione, D Levi, P Sundararajan - 2nd Annual Military and Aerospace Applications of ..., 1999 - tpoint.net
 ... specification of details, unlike automatic **CAD** tools ... of such changes to the **circuit configuration** can be ... Like in-circuit emulators, the results are produced by ...
[Cited by 118](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

[... of Configuration Controllers for Dynamically Reconfigurable Logic Systems Using the DCS CAD ...](#) - [group of 6 »](#)

D Robinson, P Lysaght - Field Programmable Logic and Applications, P. Lysaght, J. ... - Springer
 ... DCS (dynamic **circuit** switching) [4] is a VHDL ... G. McGregor and J. Stockwood,
 "Configuration Controller Synthesis for ... and G. McGregor, "New **CAD** Framework Extends ...
[Cited by 15](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[A Low-Power and High-Speed Dynamic PLA Circuit Configuration for Single-Clock CMOS](#) - [group of 2 »](#)

I Introduction - IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—I: FUNDAMENTAL ..., 1999 - visi.ee.nsysu.edu.tw
 ... Modern **CAD** tools are required to support the integration ... The proposed **PLA configuration**,
 us- ing one NAND gate ... This **circuit** has been fabricated in a 1.0- m ...
[Cited by 7](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#) - [BL Direct](#)

[Modern CAD methods for analysis of switched networks](#) - [group of 3 »](#)

J Vlach, A Opal - Circuits and Systems I: Fundamental Theory and Applications, ..., 1997 - ieeexplore.ieee.org
 ... the best method for general **CAD** application. ... The matrices depend on the **circuit configuration**, the clock ... the continuous nature of the **circuit** into consideration ...
[Cited by 13](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Explicit evaluation of short circuit power dissipation for CMOS logic structures](#) - [group of 4 »](#)

S Turgis, N Azemard, D Auvergne - Proceedings of the 1995 international symposium on Low power ..., 1995 - portal.acm.org
 ... to allow the output to reach a stable **state** before T ... Fig3 : Short-circuit power
 dependency to the load for different internal **configuration** ratio values ...
[Cited by 14](#) - [Related Articles](#) - [Web Search](#)

[Managing design data: the five dimensions of CAD frameworks, configuration management, and product ...](#) - [group of 2 »](#)

P van den Hamer, K Lepoeter - Proceedings of the IEEE, 1996 - ieeexplore.ieee.org
 ... development may be rejected by integrated **circuit** designers, and vice ... is only fully
 tackled in **CAD** Frameworks; in software **configuration** management systems ...
[Cited by 23](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Reconfigurable Computing Application Frameworks](#) - [group of 7 »](#)

AL Slade, BE Nelson, BL Hutchings - ieeexplore.ieee.org
 ... typically not feasible with large, monolithic **CAD** tools ... Therefore, for the **circuit**
 design shown in Figure 8 ... two that were subsumed into the **configuration** of the ...
[Cited by 4](#) - [Related Articles](#) - [Web Search](#)